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Term	Documents
(19 AND 5).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	24
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Database:
US Pre-Grant Publication Full-Text Database
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L21	L19 and l5	24	L21
L20	l3 and boolean	16	L20
L19	L17 and l6	24	L19
L18	L17 and l5	183	L18
L17	l2 and boolean	361	L17
L16	l4 and boolean	15	L16
L15	L13 and l6	77	L15
L14	L13 and l5	579	L14
L13	L12 and (decod$3 or predecod$3)	1507	L13
L12	l2 and load$5	1695	L12
L11	l2 and l5	638	L11

L10	L8 and l6	25	L10
L9	L8 and l5	80	L9
L8	load$5 and l4	108	L8
L7	predecod$ and l4	23	L7
DB=PGPB,USPT; PLUR=YES; OP=OR			
L6	(712/14,22,24,25,225)![CCLS]	1200	L6
L5	(712/2-300)[CCLS]	11287	L5
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L4	L3 and (simd or single near1 instruction$1)	109	L4
L3	L2 and (vliw$1 or very near1 large near1 instruction$1)	124	L3
L2	L1 and register$1 near2 (group$4 or set$1 or block$1 or file$1)	1914	L2
L1	(arithmetic$3 or zero or overflow or carry or barrow or psw) near5 (flag$4 or bit$1) near8 condition$3	3933	L1

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» Key

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IEE JNL	IEE Journal or Magazine
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IEEE STD	IEEE Standard

Select Article Information

1. **A microprocessor with a 128-bit CPU, ten floating-point MAC's, four floating-point dividers, decoder**
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